LCD Specification

LCD Group

LQ084S3LG01 TFT-LCD Module

Product Specification August 2007

Strong 2 SVGA LCD Module featuring 400 nits brightness with 600:1 contrast. Full Specifications Listing



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	MOBILE LIQUID CRYSTAL DISPLAY GROUP	APPLICABLE GROUP
	SHARP CORPORATION	MOBILE LIQUID CRYSTAL DISPLA
	SPECIFICATION	GROUP
	DEVICE SPECIFICATION F	OR
		OR
	DEVICE SPECIFICATION F	OR

DATE	
	PRESENTED
ВУ	BY K. Jamamolo
	K. YAMAMOTO
	General Manager
	Mobile LCD Merchandise Design Center

WUXI SHARP ELECTRONIC COMPONENTS CO., LTD.

CUSTOMER'S APPROVAL



RECORDS OF REVISION

MODEL No: LQ084S3LG01 SPEC No: LCY-W07802

	NO.	PAGE	SUMMARY	NOTE
2007. 8.14		-	-	$1^{ m st}$ Issue
	<u> </u>			
		<u> </u>		



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1. Application

This specification applies to the color TFT-LCD module, LQ084S3LG01.

2. Overview

This module is a color active matrix LCD module incorporating amorphous silicon TFT (<u>Thin Film Transistor</u>). It is composed of a color TFT-LCD panel, driver ICs, control circuit, power supply circuit and a back light unit. Graphics and texts can be displayed on a $800 \times RGB \times 600$ dots panel with about 16 million colors by using LVDS (<u>Low Voltage Differential Signaling</u>) and supplying +3.3V DC supply voltages for TFT-LCD panel driving and supply voltage for backlight.

(Note: Backlight-driving DC/AC inverter is not built in this module.)

- a) Fine images with stripe aligned 480,000 pixels on 8.4 inch diagonal screen
- b) Adapting a wide viewing angle technology [best viewing angle: 6 o'clock direction]
 - (12 o'clock direction is also available by the function to flip the screen horizontally or vertically)
- c) High contrast, thanks to active matrix drive system
- d) AG(Anti Glare) polarizing filter
- e) Light and slim compact module
- f) Natural coloring reproducibility by employing normally-white-mode, which has good nature in coloring
- g) Image inversion both horizontally and vertically
- h) This module is adapted to RoHS compliance.

3. Mechanical Specifications

Parameter	Specifications	Unit
Display size	21.3 (Diagonal)	cm
	8.4 (Diagonal)	Inch
Active area	170.4 (H)×127.8 (V)	mm
Pixel format	800 (H)×600 (V)	Pixel
	(1 pixel=R+G+B dots)	
Pixel pitch	0.213 (H)×0.213 (V)	mm
Pixel configuration	R, G, B vertical stripe	
Display mode	Normally white	
Unit outline dimensions *1	199.5(W)×149.5(H)×11.6(D)	mm
Mass	405(MAX)	g
Surface treatment	Anti-glare and hard-coating	

^{*1.}Note: excluding back light cables.

The thickness of module (D) doesn't contain the projection.

Outline dimensions are shown in Fig.1.



4. Input Terminals

4-1. TFT-LCD panel driving

CN1 (Interface signals and +3.3V DC power supply)

Using connectors : 20186-020E-11 (I-PEX)

Corresponding connectors : FI-SE20ME(Japan Aviation Electronics Industry, Ltd.)

FI-SE20S (Japan Aviation Electronics Industry, Ltd.)

Using LVDS Receiver : Contained in a control IC. [THC63LVDF84A(Thine) compatible]

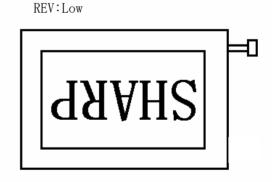
Corresponding LVDS Transmitter : THC63LVDM83R(Thine) or compatible

Pin No.	Symbol	Function	Remark
1	Vcc	+3.3V Power supply	
2	Vcc	+3.3V Power supply	
3	GND	Ground	
4	GND	Ground	
5	RxIN0-	LVDS CH0 data signal (-)	LVDS
6	RxIN0+	LVDS CH0 data signal (+)	LVDS
7	GND	Ground	
8	RxIN1-	LVDS CH1 data signal (-)	LVDS
9	RxIN1+	LVDS CH1 data signal (+)	LVDS
10	GND	Ground	
11	RxIN2-	LVDS CH2 data signal (-)	LVDS
12	RxIN2+	LVDS CH2 data signal (+)	LVDS
13	GND	Ground	
14	RxCLKIN-	LVDS CK- data signal (-)	LVDS
15	RxCLKIN+	LVDS CK+ data signal (+)	LVDS
16	GND	Ground	
17	RxIN3-	LVDS CH3 data signal (-)	LVDS
18	RxIN3+	LVDS CH3 data signal (+)	LVDS
19	REV	Reversing terminal	
20	LVDS_SET	SELLVDS	[Note1]

^{*} As for the pin No.1 of this connector, please refer to the figure on page 20.

[Note1]







4-2 Data Mapping

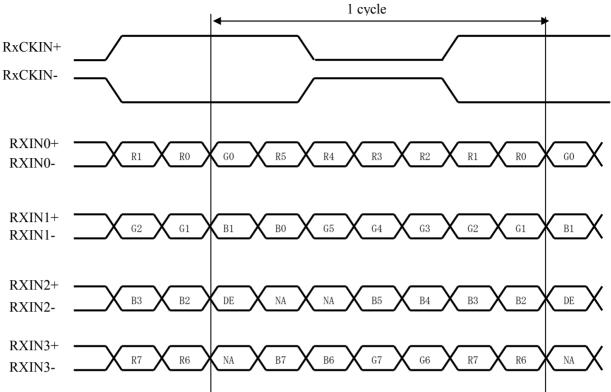
1) 8 bit input

[Note1] pin assignment with SELLVDS pin (Thine:THC63LVDM83R)

Tran	smitter	20pin I	LVDS_SET
Pin No	Data	=H (3.3V)	=L (GND) or Open
51	TA0	R0 (LSB)	R2
52	TA1	R1	R3
54	TA2	R2	R4
55	TA3	R3	R5
56	TA4	R4	R6
3	TA5	R5	R7 (MSB)
4	TA6	G0 (LSB)	G2
6	TB0	G1	G3
7	TB1	G2	G4
11	TB2	G3	G5
12	TB3	G4	G6
14	TB4	G5	G7 (MSB)
15	TB5	B0 (LSB)	B2
19	TB6	B1	В3
20	TC0	B2	B4
22	TC1	В3	B5
23	TC2	B4	B6
24	TC3	B5	B7 (MSB)
27	TC4	(NA)	(NA)
28	TC5	(NA)	(NA)
30	TC6	DE	DE
50	TD0	R6	R0 (LSB)
2	TD1	R7 (MSB)	R1
8	TD2	G6	G0 (LSB)
10	TD3	G7 (MSB)	G1
16	TD4	В6	B0 (LSB)
18	TD5	B7 (MSB)	B1
25	TD6	(NA)	(NA)

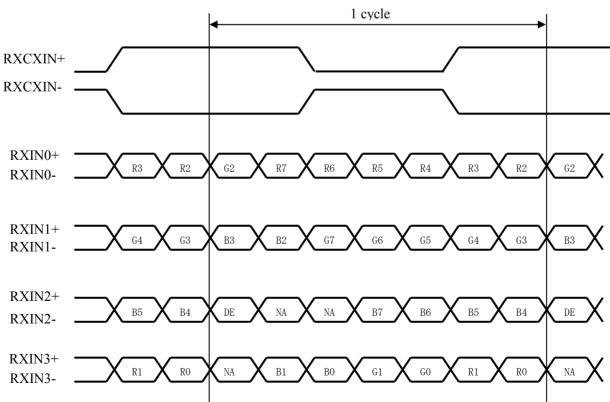


< SELLVDS = H >



DE : Display Enable NA : Not Available

< SELLVDS = L or Open >



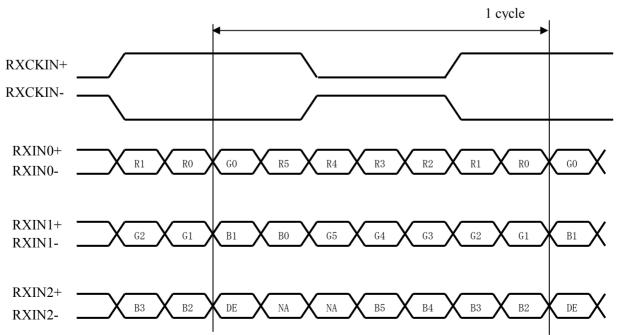
DE : Display Enable NA : Not Available



2) 6 bit input

[Note1] pin assignment with SELLVDS pin (Thine:THC63LVDM83R)

Tran	smitter	20pin SELLVDS	
Pin No	Data	=H (3.3V)	=L (GND) or Open
51	TA0	_	R0 (LSB)
52	TA1	_	R1
54	TA2	_	R2
55	TA3	_	R3
56	TA4	_	R4
3	TA5	_	R5 (MSB)
4	TA6	_	G0 (LSB)
6	TB0	_	G1
7	TB1	_	G2
11	TB2	_	G3
12	TB3	_	G4
14	TB4	_	G5 (MSB)
15	TB5	_	B0 (LSB)
19	TB6	_	B1
20	TC0	_	B2
22	TC1	_	В3
23	TC2	_	B4
24	TC3	_	B5 (MSB)
27	TC4	_	(NA)
28	TC5	_	(NA)
30	TC6	_	DE
50	TD0	_	GND
2	TD1	_	GND
8	TD2	_	GND
10	TD3	_	GND
16	TD4	_	GND
18	TD5	_	GND
25	TD6	_	(NA)



DE : Display Enable NA : Not Available



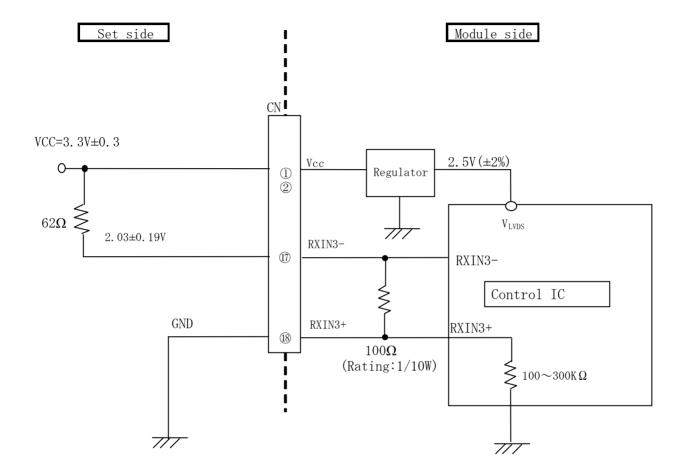
≪ About the connection of RXIN3+ and RXIN3-, in case of supplying 6bit signals. ≫

• RXIN3- : Please keep the input voltage higher than 1.7V, but less than the power supply

voltage (VLDS+0.3V) of the control IC.

• RXIN3+ : Please be connected to the GND.

(Terminal processing example)

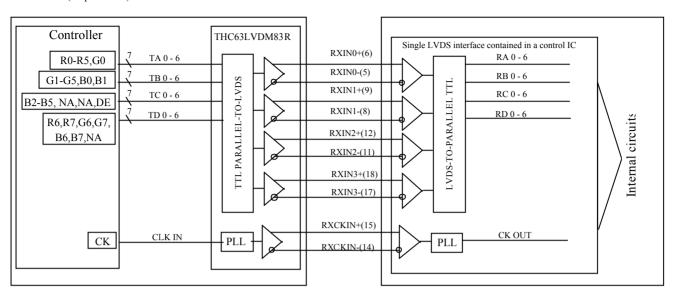




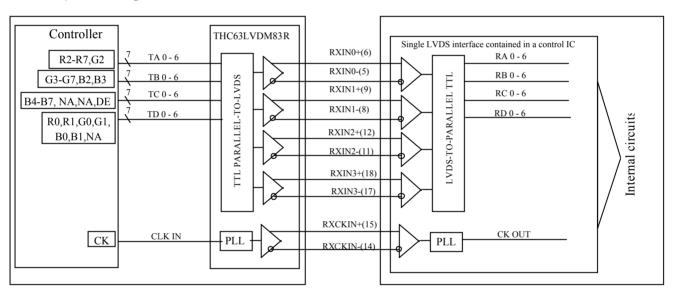
(Computer Side)

(TFT-LCD side)

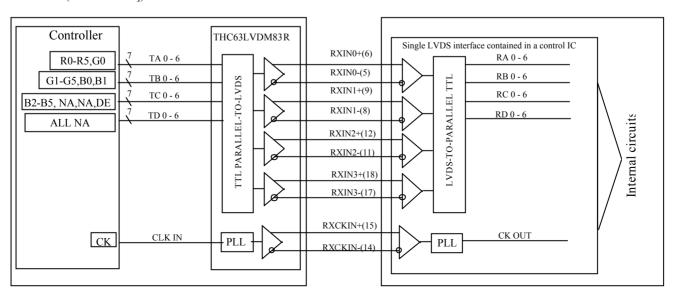
①8Bit Mode SELLVDS=H (20 pin=3.3V)



②8Bit Mode SELLVDS=L(GND or OPEN])



③6Bit Mode SELLVDS=L (GND or OPEN])





4-4. Backlight

The module-side connector : BHR-04VS-1 (JST)

The user-side connector : SM0 4 (4.0)B-BHS (JST)

Pin no.	symbol	Function					
1	VHIGH	Power supply for lamp 1	(High voltage side)				
2	VHIGH	Power supply for lamp 2	(High voltage side)				
3	NC	Open					
4	VLOW	Power supply for lamp 1,2	(Low voltage side)				

[Note]

The input voltage wave forms to terminal (1) and terminal (2) should be in a same phase.

It has the possibility to discharge abnormally between the terminals in case of input in a reversed phase.

5 Absolute Maximum Ratings

Parameter	Symbol	Condition	Ratings	Unit	Remark
Input voltage	VI	Ta=25°C	-0.3 ~ Vcc+0.3	V	[Note 1]
Supply voltage	Vcc	Ta=25°C	0 ~ +4.0	V	-
Storage temperature	Tstg	-	- 30 ∼ +80	°C	[Note 2,3,4,5]
Operating temperature (Panel surface)	Topp	-	- 30 ∼ +80	°C	

Note 1 CK, R0 ~ R5, G0 ~ G5, B0 ~ B5 and ENAB

[Note 2] Preserve it within this range.

[Note 3] Humidity: Less than 95%RH at $Ta \le 40$ °C and

Maximum wet-bulb temperature must not exceed 39°C at Ta>40°C, with no condensation.

[Note 4] The operating temperature only guarantees operation of the circuit. For contrast, speed response, and other factors related to display quality, judgment is done using the ambient temperature Ta =+25°C. [Note 5] This rating applies to all parts of the module and should not be exceed.



6. Electrical Characteristics

6-1. TFT-LCD panel driving

 $Ta=25^{\circ}C$

Parameter		Symbol	Min.	Typ.	Max.	Unit	Remark
Supply voltage		Vcc	+3.0	+3.3	+3.6	V	[Note1]
Current dissipation		Icc	_	250	380	mA	[Note2]
Input voltage ("High" s	tate)	Vrih	Vcc×0.7			V	[Note3]
Input voltage ("Low" state)		Vril			Vcc×0.3	V	
Permissive input ripple v	oltage	V_{RF}	_	_	100	mVp-p	
Terminal resistor		R_{T}	_	100	_	Ω	Differential input
Differential input	High	V _{IH}	_	_	100	mV	
threshold voltage	Low	$V_{ m IL}$	-100	_	_	mV	

[Note1]

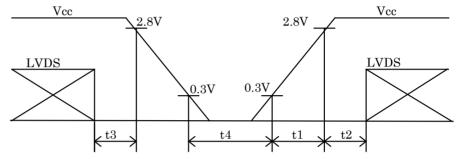
On-off sequences of Vcc and data

0 <t1≤15ms

0 <t2<50ms

0 <t3<50ms

1s < t4

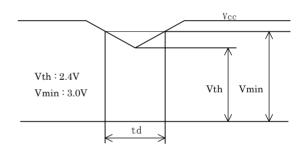


Dip conditions for supply voltage Vmin,Vth=3.0V,2.4V

1) Vth \leq Vcc < Vmin Td \leq 10ms

2) Vcc<Vth

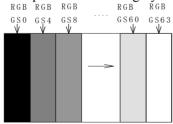
Vcc-dip conditions should also follow the on-off conditions.



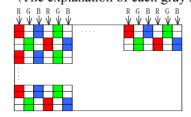
[Note2] Typical current situation : 64-gray-bar pattern

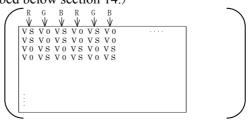
Vcc=+3.3V, fck=65MHz, Ta=25°C

The explanation of each gray scale is described below section 14.



When VS(V63) and V0 stripe pattern are displayed alternately at intervals of two lines. (The explanation of each gray scale is described below section 14.)





[Note3] Terminal: REV



6-2. Backlight

The backlight system is an edge-lighting type with two CCFTs (Cold Cathode Fluorescent Tube).

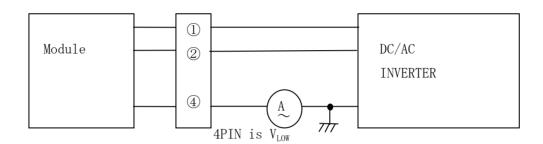
The characteristics of single lamp are shown in the following table.

*Recommended inverter(TDK : CXA-0463)

Ta=25°C

Parameter	Symbol	Min.	Тур.	Max.	Unit	Remarks
Lamp voltage	VL	380	420	460	V rms	IL=6mA rms
Lamp current	IL	3.0	6.0	6.5	mA rms	[Note6-7]
Lamp power consumption	PL	-	2.5	-	W	
Lamp frequency	fL	50	-	80	KHz	Recommend 55kHz
Kick-off voltage	Vs	-	-	890	V rms	Ta=25°C [Note6-8]
[Note6-1]		-	-	1070		Ta=-30°C [Note6-8]
Lamp life time	LL	-	50,000	_	hour	[Note6-9]

[Note6-7] Lamp current is measured with current meter for high frequency as shown below.



[Note6-8] The open output voltage of the inverter shall be maintained for more than 1sec;

otherwise the lamp may not be turned on.

The input voltage wave forms to terminal ① and terminal ② should be in a same phase. It has the possibility to discharge abnormally between the terminals in case of input of a reversed phase. For the sake of the safety, please so design the inveter as to prevent abnormal discharge when one of the two lamps is broken or reaches the end of life.

[Note6-9] The data for lamp is for your reference, because lamp is consumable component.

- a) Lamp life time is defined by either 1 or 2 below. (Continuous turning on at Ta=25°C, IL=6mA rms)
 - ① When a brightness of lamp surface became 50% of the initial value under the standard condition.
 - ②When a kick-off voltage in Ta= -30 $^{\circ}$ C exceeded maximum value ((1070) Vrms) at the output of inverter circuitry
- b) In case of operating under lower temp. environment, the lamp exhaustion is accelerated and the brightness becomes lower.

(Continuous operating for around 1 month under lower temp. condition may reduce the brightness to half of the original brightness.)

In case of such usage under lower temp. environment, periodical lampexchange is recommended

[Note] The performance of the backlight, for example life time or brightness, is much influenced by the characteristics of the DC-AC inverter for the lamp. When you design or order the inverter, please make sure that an inadequate lighting caused by the mismatch of the backlight and the inverter (miss-lighting, flicker, etc.) never occur. When you confirm it, the module should be operated in the same condition as it is installed in your instrument.

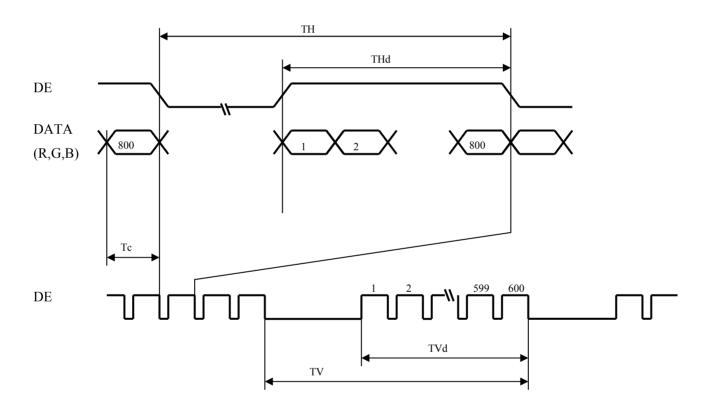


7. Timing characteristics of input signals

7-1-1. Timing characteristics

Parameter	Symbol	Min.	Тур.	Max.	Unit	Remark
Frequency	1/Tc	35	40	42	MHz	
Horizontal period	TH	832	1056	1395	clock	
		20.8	26.4	39.9	μs	
Horizontal period (High)	THd	800	800	800	clock	
Vertical period	TV	628	666	798	line	[Note]
		-	17.6	-	ms	
Vertical period (High)	TVd	600	600	600	line	

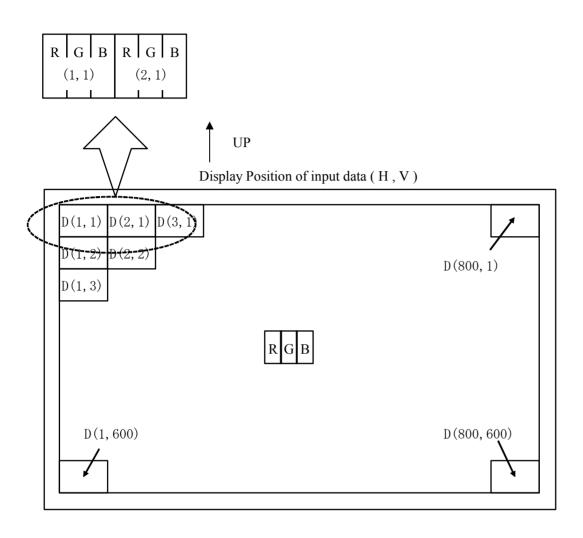
[Note1] In case of using the long vertical period, the deterioration of display quality, flicker etc. may occur.





7-2 Input Data Signals and Display Position on the screen

Graphics and texts can be displayed on a panel with 16M colors by supplying 24 bit data signal (8bit/color [253 gray scales] \times 3).





8. Input Signals, Basic Display Colors and Gray Scale of Each Color

								Data signal																		
	Colors & Gray scale	Gray Scale	R0	R1	R2	R3	R4	R5	R6	R7	G0	G1	G2	G3	G4	G5	G6	G7	В0	B1	B2	В3	B4	В5	В6	В7
Basic Color	Black	_	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	Blue	_	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	X	X	1	1	1	1	1	1
	Green	_	0	0	0	0	0	0	0	0	X	X	1	1	1	1	1	1	0	0	0	0	0	0	0	0
	Cyan	_	0	0	0	0	0	0	0	0	X	X	1	1	1	1	1	1	X	X	1	1	1	1	1	1
	Red	_	X	X	1	1	1	1	1	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	Magenta	_	X	X	1	1	1	1	1	1	0	0	0	0	0	0	0	0	X	X	1	1	1	1	1	1
	Yellow	_	X	X	1	1	1	1	1	1	X	X	1	1	1	1	1	1	0	0	0	0	0	0	0	0
	White	_	X	X	1	1	1	1	1	1	X	X	1	1	1	1	1	1	X	X	1	1	1	1	1	1
Gray Scale of Red	Black	GS0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	仓	GS1	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	Darker	GS2	0	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	Û	\downarrow		V						↓						↓										
	Û	\downarrow	\downarrow						↓						↓											
	Brighter	GS250	1	0	0	1	1	1	1	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	Û	GS251	1	1	0	1	1	1	1	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	Red	GS252	X	X	1	1	1	1	1	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
	Black	GS0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
G	Û	GS1	0	0	0	0	0	0	0	0	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
ray S	Darker	GS2	0	0	0	0	0	0	0	0	0	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0
Scale	Û	\downarrow	↓						↓						Ψ											
Gray Scale of Green	Û	\rightarrow	\						↓						↓											
Gree	Brighter	GS250	0	0	0	0	0	0	0	0	1	0	0	1	1	1	1	1	0	0	0	0	0	0	0	0
n	Û	GS251	0	0	0	0	0	0	0	0	1	1	0	1	1	1	1	1	0	0	0	0	0	0	0	0
	Green	GS252	0	0	0	0	0	0	0	0	X	X	1	1	1	1	1	1	0	0	0	0	0	0	0	0
	Black	GS0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
Gray Scale of Blue	Û	GS1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	0	0	0	0	0	0	0
	Darker	GS2	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	0	0	0	0	0	0
	仓	→	V					V						V												
le of	Û	→	V					V						↓												
Blu	Brighter	GS250	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	0	0	1	1	1	1	1
e	Û	GS251	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	1	1	0	1	1	1	1	1
	Blue	GS252	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	X	X	1	1	1	1	1	1

0 : Low level voltage,

1 : High level voltage.

X:Don't care.

Each basic color can be displayed in 253 gray scales from 8 bit data signals. According to the combination of total 24 bit data signals, the 16M color display can be achieved on the screen.



9. Optical Characteristics

 $Ta=25^{\circ}C$, Vcc=+3.3V

Para	ameter	Symbol	Condition	Min.	Тур.	Max.	Unit	Remark
Viewing	Horizontal	θ 21	CR≧10	50	65	ı	Deg.	[Note1,4]
angle		θ 22		50	65	-	Deg.	
range	Vertical	θ 11		40	55	ı	Deg.	
		θ 12		45	60	ı	Deg.	
Contr	ast ratio	CR	the best viewing angle	400	600	-	-	[Note2,4]
Response	rise	τr	$\theta = 0^{\circ}$	-	5	15	ms	[Note3,4]
time	fall	τd		-	21	40	ms	
Chrom	aticity of	X	$\theta = 0^{\circ}$	0.263	0.313	0.363	-	[Note4,5]
White		Y	<i>θ</i> –0	0.279	0.329	0.379	-	
	ce of white	Y_L	$\theta = 0^{\circ}$	320	400	-	cd/m ²	
	n of the best ng angle	-			6		o'clock	[Note6]

%The measurement shall be executed 30 minutes after lighting at rating. (IL = 6mArms \times 2)

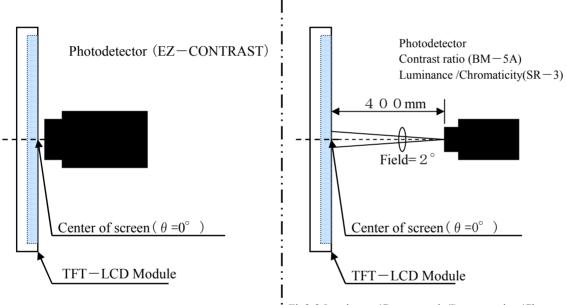


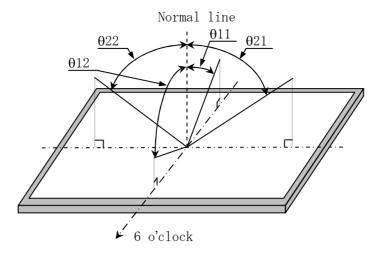
Fig2-1 Viewing angle measurement method

Fig2-2 Luminance/Contrast ratio/Response time/Chromaticity measurement method

Fig2 Optical characteristics measurement method



[Note1] Definitions of viewing angle range:



The best viewing angle of this module is slightly leaned to 6 o'clock from normal line.

In the field where θ_{12} exceeds this angle, gray-scale is reversed partially.

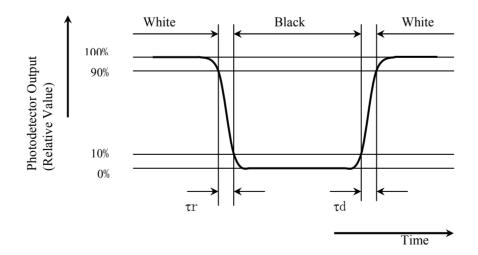
The gray-scale in the field of 12 o'clock direction is brighter than that of 6 o'clock direction and isn't reversed.

[Note2] Definition of contrast ratio:

The contrast ratio is defined as the following.

[Note3] Definition of response time:

The response time is defined as the following figure and shall be measured by switching the input signal for "black" and "white".



[Note4] This shall be measured at center of the screen.



10. Handling Precautions

- a) Be sure to turn off the power supply when inserting or disconnecting the cable.
- b) Be sure to design the cabinet so that the module can be installed without any extra stress such as warp or twist.
- c) Since the front polarize is easily damaged, pay attention not to scratch it.
- d) Since long contact with water may cause discoloration or spots, wipe off water drop immediately.
- e) When the panel surface is soiled, wipe it with absorbent cotton or other soft cloth.
- f) Since the panel is made of glass, it may break or crack if dropped or bumped on hard surface. Handle with care.
- g) Since CMOS LSI is used in this module, take care of static electricity and take the human earth into consideration when handling.
- h) Make sure the four mounting holes of the module are grounded sufficiently. Take electro-magnetic interference (EMI) into consideration.
- i) The module has some printed circuit boards (PCBs) on the back side. Take care to keep them form any stress or pressure when handling or installing the module; otherwise some of electronic parts on the PCBs may be damaged.
- j) Observe all other precautionary requirements in handling components.
- k) Since it is necessary to remove the screw on the back of a module before performing lamp exchange, please take a cabinet design into consideration.
- When some pressure is added onto the module from rear side constantly, it causes display non-uniformity issue, functional defect, etc. So, please avoid such design.
- m) When handling LCD modules and assembling them into cabinets, please be noted that long-term storage in the environment of oxidization or deoxidization gas and the use of such materials as reagent, solvent, adhesive, resin, etc. which generate these gasses, may cause corrosion and discoloration of the LCD modules.
- n) The polarizer surface on the panel is treated with Anti-Glare for low reflection. In case of attaching protective board over the LCD, be careful about the optical interface fringe etc. which degrades display quality.



11. Packing form

a) Maximum number of Carton being stuck: Max. 6 cartons

b) Maximum number of product contained: 10 Unit

c) Carton size: 408mm (W) x 203mm (D) x 244mm (H)

d) Total mass (for 20Unit): 6kg

e) Fig.3 shows packaging form.

12. Reliability test items

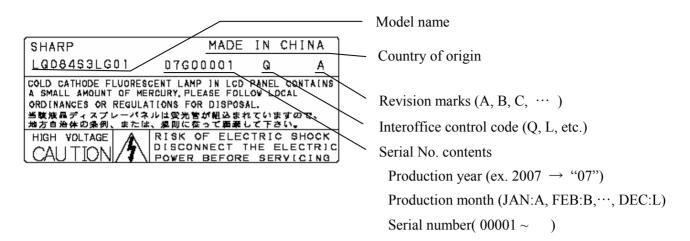
No.	Test parameter	Conditions								
1	High temperature storage test	Leaves the module at Ta=80°C for 240h								
2	Low temperature storage test	Leaves the module at Ta=-30°C for 240h								
3	High temperature	Operates the module at Ta=40°C; 95%RH for 240h								
	& high humidity operation test	(No condensation)								
4	High temperature operation test	Operates the module with +80°C at panel surface for 240h								
5	Low temperature operation test	Operates the module at Ta=-30°C for 240h								
6	Vibration test	Frequency: 10~57Hz/Vibration width (one side):0.15mm								
	(non- operating)	: 58~500Hz/ acceleration:19.6m/s ² (2G)								
		Sweep time: 11 minutes								
		Test period: 1 hour for each direction of X,Y,Z (total 3 hours)								
7	Shock test	Max. acceleration: 686m/s ² (70G)								
	(non- operating)	Pulse width: 6ms, sine wave								
		Direction : $\pm X, \pm Y, \pm Z$ once for each direction.								

[Result Evaluation Criteria]

Under the display quality test conditions with normal operation state, these shall be no change which may affect practical display function.



1) Lot No.label:



- 2) Adjusting volume have been set optimally before shipment, so do not change any adjusted value. If adjusted value is changed, the specification may not be satisfied.
- 3) Disassembling the module can cause permanent damage and should be strictly avoided.
- 4) Please be careful since image retention may occur when a fixed pattern is displayed for a long time.
- 5) When any question or issue occurs, it shall be solved by mutual discussion.
- 6) Don't use or store the module in corrosive gas environment.
- 7) The LCD has the nature that its performance is degradation by ultra-violet light. Don't leave the LCD module in direct sunlight or strong ultra violet ray. (A fall of the contrast, a shift of the chromaticity)

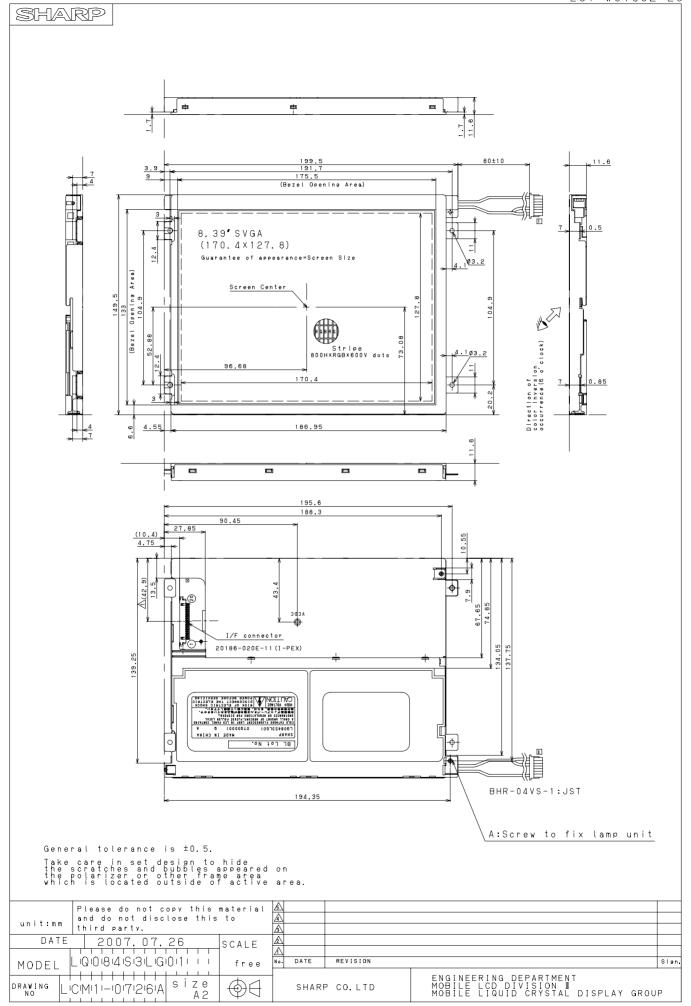
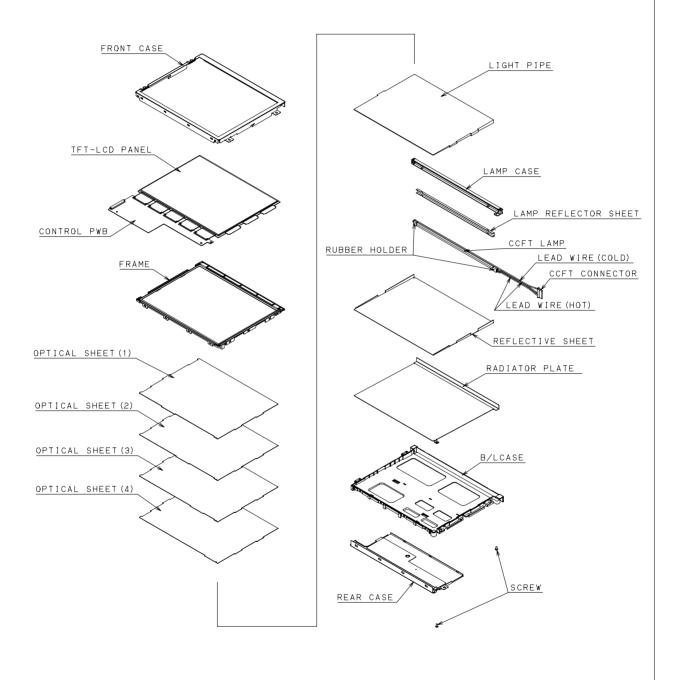


FIG. 1 OUTLINE DIMENSIONS

SHARP®



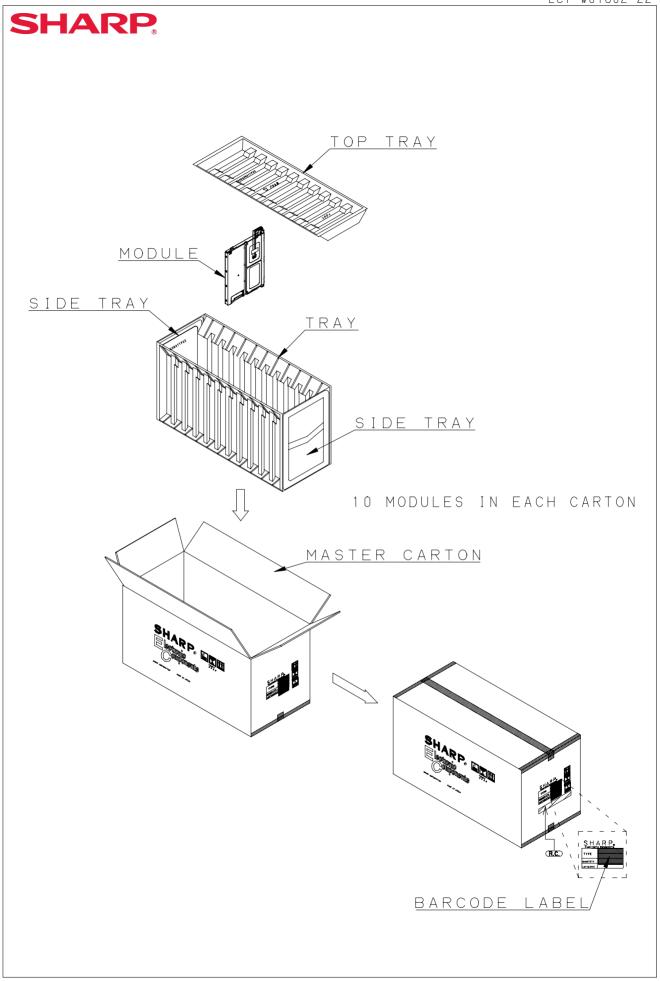


FIG. 3 PACKING FORM

LCD Specification

LCD Group

SHARP

NORTH AMERICA

Sharp Microelectronics of the Americas 5700 NW Pacific Rim Blvd. Camas, WA 98607, U.S.A. Phone: (1) 360-834-2500 Fax: (1) 360-834-8903 www.sharpsma.com

TAIWAN

Sharp Electronic Components (Taiwan) Corporation 8F-A, No. 16, Sec. 4, Nanking E. Rd. Taipei, Taiwan, Republic of China Phone: (886) 2-2577-7341 Fax: (886) 2-2577-7326/2-2577-7328

CHINA

Sharp Microelectronics of China (Shanghai) Co., Ltd.
28 Xin Jin Qiao Road King Tower 16F
Pudong Shanghai, 201206 P.R. China
Phone: (86) 21-5854-7710/21-5834-6056
Fax: (86) 21-5854-4340/21-5834-6057
Head Office:
No. 360, Bashen Road,

200131 P.R. China Email: smc@china.global.sharp.co.jp

Waigaoqiao Free Trade Zone Shanghai

Xin Development Bldg. 22

EUROPE

Sharp Microelectronics Europe
Division of Sharp Electronics (Europe) GmbH
Sonninstrasse 3
20097 Hamburg, Germany
Phone: (49) 40-2376-2286
Fax: (49) 40-2376-2232

SINGAPORE

www.sharpsme.com

Sharp Electronics (Singapore) PTE., Ltd. 438A, Alexandra Road, #05-01/02 Alexandra Technopark, Singapore 119967 Phone: (65) 271-3566 Fax: (65) 271-3855

KOREA

Sharp Electronic Components (Korea) Corporation RM 501 Geosung B/D, 541 Dohwa-dong, Mapo-ku Seoul 121-701, Korea Phone: (82) 2-711-5813 ~ 8 Fax: (82) 2-711-5819

JAPAN

Sharp Corporation
Electronic Components & Devices
22-22 Nagaike-cho, Abeno-Ku
Osaka 545-8522, Japan
Phone: (81) 6-6621-1221
Fax: (81) 6117-725300/6117-725301
www.sharp-world.com

HONG KONG

Sharp-Roxy (Hong Kong) Ltd.
3rd Business Division,
17/F, Admiralty Centre, Tower 1
18 Harcourt Road, Hong Kong
Phone: (852) 28229311
Fax: (852) 28660779
www.sharp.com.hk
Shenzhen Representative Office:
Room 13B1, Tower C,
Electronics Science & Technology Building
Shen Nan Zhong Road
Shenzhen, P.R. China
Phone: (86) 755-3273731
Fax: (86) 755-3273735

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