|   |   |                             | SPEC.NO.            | TQ3C-8EACO  | -E1CUR06-00            |
|---|---|-----------------------------|---------------------|---|------------------------|
|   |   |                             | DATE                | December  | • 26,2001              |
| SPEC  |   |                             |                     |   |                        |
|   |   |                             |                     |   |                        |
|   | FO  | R :                         |                     |   |                        |
|   | <u> </u>  | <u>C G O 6 2 H V</u>        | <u> 1 A A - A 2</u> | <u>1</u>  |                        |
|   |   | CONTENI                     | S                   |   |                        |
| <ol> <li>Application</li> <li>Construction</li> <li>Mechanical Sy</li> <li>Absolute Max</li> <li>Electrical Cl</li> <li>Optical Chara</li> <li>Circuit Block</li> <li>Interface Sig</li> <li>Interface Tim</li> <li>Data and Screen</li> <li>Input Timing</li> <li>Supply Voltage</li> <li>Backlight Chara</li> <li>Warranty</li> <li>Precautions</li> <li>Reliability</li> <li>Outline Draw</li> </ol> | pecifications<br>imum Ratings<br>haracteristic<br>acteristics<br>k Diagram<br>gnals<br>ming Chart<br>een<br>Characterist<br>ge Sequence C<br>aracteristics<br>dentification<br>for Use<br>Data / Enviro | s<br>ics<br>ondition        | I<br>D<br>K<br>Ha   | NTAT<br>ssued<br>ate: DEC.2<br>XYUDEER<br>ayato LCD D | 8.2001<br>E<br>ivision |
|   |   |                             | KA                  | DCERA CORPORAT<br>Goshima Hayato<br>Division          |                        |
|   | ification is<br>yocera before   | subject to cha<br>ordering. | ange without        | notice.   |                        |
| Original  | Designed by   | :Engineering                | Dept.               | Confirmed by  | v :QA Dept.            |
| Issue Data  | Prepared  | Checked                     | Approved            | Checked   | Approved               |
| December 26,2001  | T. Yamaguchi  | 4 masumoto                  | H.Ohno              | S. Haprohi  | y. yoshita             |

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## Caution

- 1. This Kyocera LCD module has been specifically designed for use only in electronic devices in the areas of audio control, office automation, industrial control, home appliances, etc. The modules should not be used in medical applications where module failure could result in physical harm or loss of life, and Kyocera expressly disclaims any and all liability relating in any way to the use of the module in such medical applications.
- 2. Customer agrees to indemnify, defend and hold Kyocera harmless from and against any and all actions, claims, losses, damages, liabilities, awards, costs, and expenses, including legal fees, resulting from or arising out of Customer's use, or sale for use, of Kyocera modules in medical applications.
- 3. Kyocera shall have the right, which Customer hereby acknowledges, to immediately scrap or destroy tooling for Kyocera modules for which no Purchase Orders have been received from the Customer in a two-year period.

Revision Record

| D        |      | Design | ed by: | : Engineering Dept. Confirmed by: QA Dept. |            |         | QA Dept. |
|----------|------|--------|--------|--|------------|---------|----------|
| Date     |      | Prepa  | red    | Checked                                    | Approved   | Checked | Approved |
|          |      |        |        |  |            |         |          |
| Rev. No. | Date |        | Page   |  | Descriptio | ns      |          |
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#### 1. Application

This data sheet defines the specification for a  $(640 \times R. G. B) \times 240$  dot, STN Transmissive color dot matrix type Liquid Crystal Display with CFL backlight.

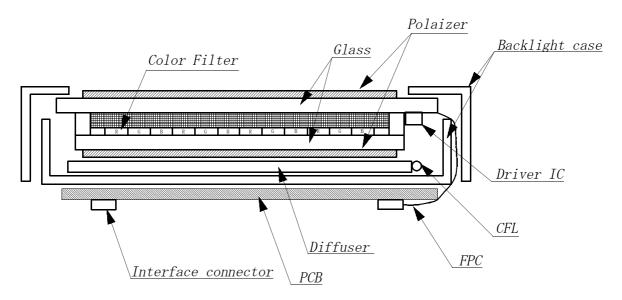
#### 2. Construction and Outline

 $(640 \times R.G.B) \times 240$  dots, COG type LCD with CFL backlight.

Backlight system : Side-edge type CFL (1tube). Inverter : Option. Recommended Inverter : PH-BLC08-K2 (HITACHI MEDIA ELECTRONICS) or equivalent.

Polarizer : Non-Glare treatment.

Additional circuit : Bias voltage circuit , Randomizing circuit



This drawing is showing conception only.

## 3. Mechanical Specifications

| ITEM                   | SPECIFICATION  | UNIT |
|------------------------|--|------|
| Outline dimensions     | 174.2 (₩) × 73.4 (H) × 7.6(PCB and components<br>not included.) (D)<br>≪Refer outline drawing in detail. | mm   |
| Effective viewing area | 149.8 (W) $\times$ 57.4 (H)  | mm   |
| Dot number             | $(640 \times R. G. B)$ (W) $\times$ 240 (H)  | Dots |
| Dot size               | 0.057 (W) $	imes$ 0.211 (H)  | mm   |
| Dot pitch              | 0.077 (W) $	imes$ 0.231 (H)  | mm   |
| Display color *1       | White *2   | —    |
| Base color *1          | Black *2   | _    |
| Mass                   | 135  | g    |

\*1 Due to the characteristics of the LC material, the color vary with environmental temperature.

\*2 Negative-type display Display data "H" :R.G.B Dots ON : White Display data "L" :R.G.B Dots OFF : Black

### 4. Absolute Maximum Ratings

4-1. Electrical absolute maximum ratings

| ITEM                           | SYMBOL                     | MIN. | MAX. | UNIT |
|--------------------------------|----------------------------|------|------|------|
| Supply voltage for logic       | VDD                        | 0    | 6.0  | V    |
| Supply voltage for LCD driving | VCONT                      | 0    | VDD  | V    |
| Input signal voltage *1        | Vin                        | 0    | VDD  | V    |
| FRM frequency                  | $\mathrm{f}_{	extsf{frm}}$ | —    | 150  | Hz   |

\*1 Input signal :CP, LOAD, FRM, DISP, D0 $\sim$ D7

4-2. Environmental absolute maximum ratings

| ITEM                  |    | SYMBOL | MIN | MAX | UNIT |
|-----------------------|----|--------|-----|-----|------|
| Operating temperature | *1 | Тор    | 0   | 50  | °C   |
| Storage temperature   | *2 | Тѕто   | -20 | 60  | °C   |
| Operating humidity    | *3 | Нор    | 10  | *4  | %RH  |
| Storage humidity      | *3 | Нѕто   | 10  | *4  | %RH  |
| Vibration             |    | _      | *5  | *5  | _    |
| Shock                 |    | _      | *6  | *6  | _    |

\*1 LCD's display quality shall not be guaranteed at the temperature range of : below 0°C and upper 40°C.

- \*2 Temp. = -20°C < 48 h , Temp = 60°C < 168 h Store LCD panel at normal temperature/humidity. Keep it free from vibration and shock. LCD panel that is kept at low or high temperature for a long time can be defective due to the other conditions, even if the temperature satisfies standard.
- \*3 Non-condensation.
- \*4 Temp. ≦ 40°C, 85% RH Max. Temp. > 40°C, Absolute Humidity shall be less than 85%RH at 40°C.

\*5

| Frequency       | 10~55 Hz    | Converted to<br>acceleration value : |
|-----------------|-------------|--------------------------------------|
| Vibration width | 0.15 mm     | $(0.3 \sim 9 \text{ m/s}^2)$         |
| Interval        | 10-55-10 Hz | 1 minute                             |

2 hours in each direction  $\rm X/Y/Z$  (6 hours as total) EIAJ ED-2531

\*6 Acceleration:  $490m/s^2$ Pulse width : 11 ms 3 times in each direction :  $\pm X/\pm Y/\pm Z$ . EIAJ ED-2531

## 5. Electrical Characteristics

VDD =  $3.3V \pm 0.3V$ , Temp. =  $0 \sim 50^{\circ}C$ 

| ITEM  | SYMBOL | CONDITION                    | MIN.   | TYP.       | MAX.   | UNIT    |
|---|--------|------------------------------|--------|------------|--------|---------|
| Supply voltage for logic                      | VDD    | _                            | 3.0    | 3.3        | 3.6    | V       |
| LCD driving voltage *1                        | Vop=   | 0 °C                         | (1.20) | —          | —      | V       |
|   | VCONT  | 25 °C                        | (1.30) | (1.80)     | (2.30) | V       |
|   |        | 50 °C                        | _      | —          | (2.40) | V       |
| Input voltage<br>(FRM, LOAD, CP, D0~D7, DISP) | Vin    | "H" level                    | 0.8VDD | —          | VDD    | V       |
| $(FRM, LOAD, CP, D0 \sim D7, D1SP)$           | vin    | "L" level                    | 0      | —          | 0.2VDD | V       |
| Input current                                 | Iin    | Vin=VDD or VSS               | -100   | —          | 100    | $\mu$ A |
| Rush current for logic                        | Irush  | When rush<br>current happens |        | 3.0A(Peak) | × 1ms  |         |
| Clock frequency                               | f cp   | _                            | 4.03   | 4.32       | 5.88   | MHz     |
| Frame frequency *2                            | f frm  | _                            | 70     | 75         | —      | Hz      |
| Current consumption<br>for logic              | IDD    | *3                           | _      | (55)       | (83)   | mA      |
| Power consumption                             | Pdisp  |                              |        | (182)      | (274)  | mW      |

- \*1 Maximum contrast is obtained by adjusting the LCD driving voltage (Vop=Vcont) while at the viewing angle of  $\theta = \phi = 0^{\circ}$
- \*2 In consideration of display quality, it is recommended that frame frequency is set in the range of 70-80Hz. When you have to use higher frame and clock frequencies, confirm the LCD's performan -ce and quality prior to finalizing the frequency values: Generally, as frame and clock frequencies become higher current consumption will get bigger and display quality will be degraded.
- \*3 VCONT = Vop, f<sub>FRM</sub> = 75Hz, f<sub>CP</sub> = 4.32MHz, Temp. = 25°C Display IDD max pattern. :

| 1      |  |
|--------|--|
|        |  |
| I<br>I |  |
| I<br>I |  |
| 239    |  |
|        |  |
| (dot)  |  |

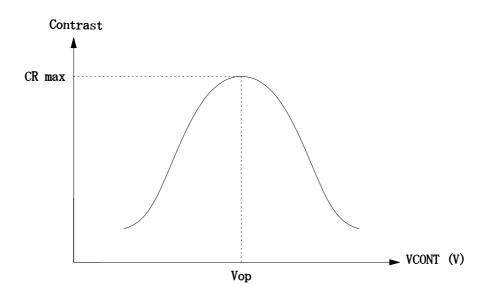
## 6. Optical Characteristics

Temp. =  $25^{\circ}C$ 

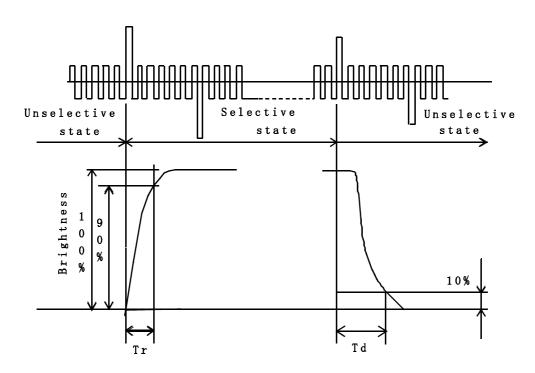
| ITEN          | ITEM                 |    | COND                        | ITION                | MIN.   | TYP.   | MAX.   | UNIT     |
|---------------|----------------------|----|-----------------------------|----------------------|--------|--------|--------|----------|
| Response      | Rise                 | Tr | $\theta = \phi = 0^{\circ}$ |                      | _      | (190)  | (290)  | ms       |
| time          | Down                 | Td | $\theta = q$                | $\phi = 0^{\circ}$   | _      | (180)  | (280)  | ms       |
| Viewing angle | e range              | θ  |                             | $\phi = 0^{\circ}$   | (-20)  | _      | (30)   | deg.     |
|               |                      | φ  | CR≧2                        | $\theta = 0^{\circ}$ | (-50)  | _      | (50)   | deg.     |
| Contrast rati | Contrast ratio       |    | $\theta = \phi = 0^{\circ}$ |                      | (15)   | (30)   | _      | —        |
| Brightness(II | Brightness(IL=5.0mA) |    | _                           |                      | (110)  | (160)  | _      | $cd/m^2$ |
| Chromaticity  | ות                   | Х  | $\theta = \phi = 0^{\circ}$ |                      | (0.48) | (0.53) | (0.58) |          |
| coordinates   | Red                  | у  |                             |                      | (0.29) | (0.34) | (0.39) |          |
|               | 0                    | Х  | $\theta = \phi = 0^{\circ}$ |                      | (0.24) | (0.29) | (0.34) |          |
|               | Green                | у  |                             |                      | (0.44) | (0.49) | (0.54) |          |
|               | Blue                 | Х  | 0 -                         | $\phi = 0^{\circ}$   | (0.11) | (0.16) | (0.21) |          |
|               | Dine                 | у  | $\sigma - c$                | p —0                 | (0.09) | (0.14) | (0.19) |          |
|               | White                | Х  | 0 -                         | $\phi = 0^{\circ}$   | (0.25) | (0.30) | (0.35) |          |
|               | White                | у  | $\sigma - \phi$             | p —0                 | (0.27) | (0.32) | (0.37) |          |

\* Optimum contrast is obtained by adjusting the LCD driving voltage (Vop=Vcont) while at the viewing angle of  $\theta = \phi = 0^{\circ}$ 

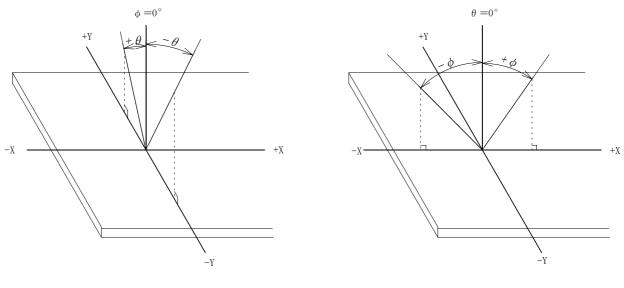
6-1. Contrast ratio is defined as follows:



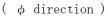
6-3. Definition of response time



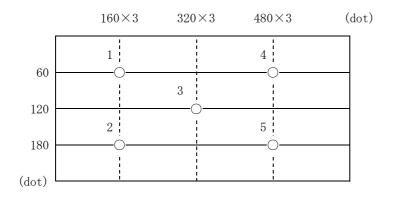
6-4. Definition of viewing angle



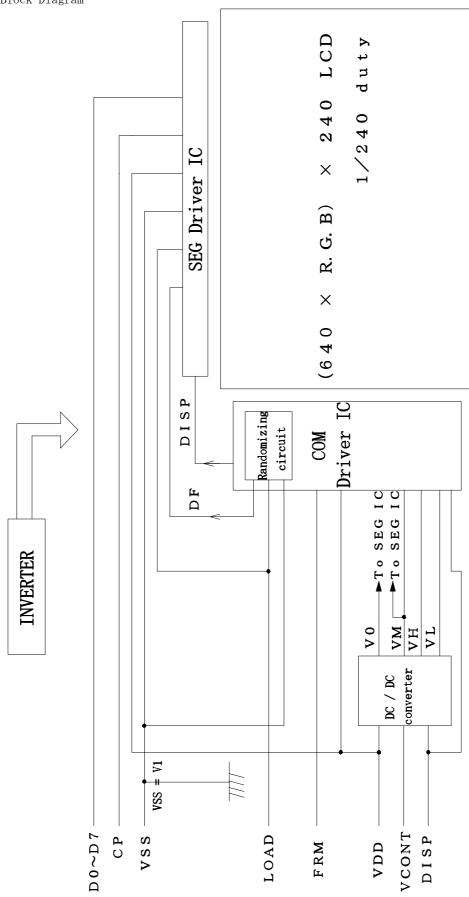
(  $\theta$  direction )



#### 6-5. Measuring points

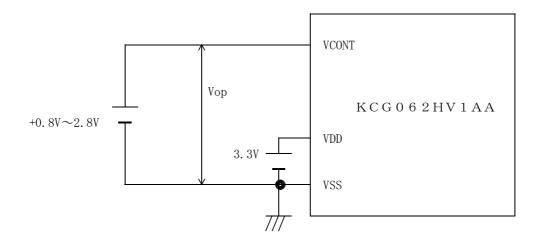


- 1) Rating is defined as the average brightness inside the viewing area.
- 2) 30 minutes after CFL is turned on. (Ambient Temp.= $25^{\circ}$ C)
- The inverter should meet the eccentric conditions;
   -Sine, symmetric waveform without spike in positive and negative.
- 4) Measuring Inverter : PH-BLC08-K2(HITACHI MEDIA ELECTRONICS)



#### 7. Circuit Block Diagram

## 7-1. Power supply



## 8. Interface signals

## 8-1. LCD

| PIN NO. | SYMBOL | DESCRIPTION                                  | LEVEL                       |
|---------|--------|--|-----------------------------|
| 1       | FRM    | Synchronous signal for driving scanning line | Н                           |
| 2       | LOAD   | Data signal latch clock                      | $H \rightarrow L$           |
| 3       | CP     | Data signal shift clock                      | $\mathbb{H} \to \mathbb{L}$ |
| 4       | DISP   | Display control signal                       | H(ON),L(OFF)                |
| 5       | VDD    | Power supply for logic                       | —                           |
| 6       | VSS    | GND  | —                           |
| 7       | VCONT  | LCD adjust voltage                           |                             |
| 8       | D7     |  |                             |
| 9       | D6     |  |                             |
| 10      | D5     |  |                             |
| 11      | D4     | Display data                                 | H(ON),L(OFF)                |
| 12      | D3     |  |                             |
| 13      | D2     |  |                             |
| 14      | D1     |  |                             |
| 15      | DO     |  |                             |
| 16      | VDD    | Power supply for logic                       | _                           |
| 17      | VDD    |  |                             |
| 18      | VSS    | GND  | _                           |
| 19      | VSS    |  |                             |
| 20      | VSS    |  |                             |

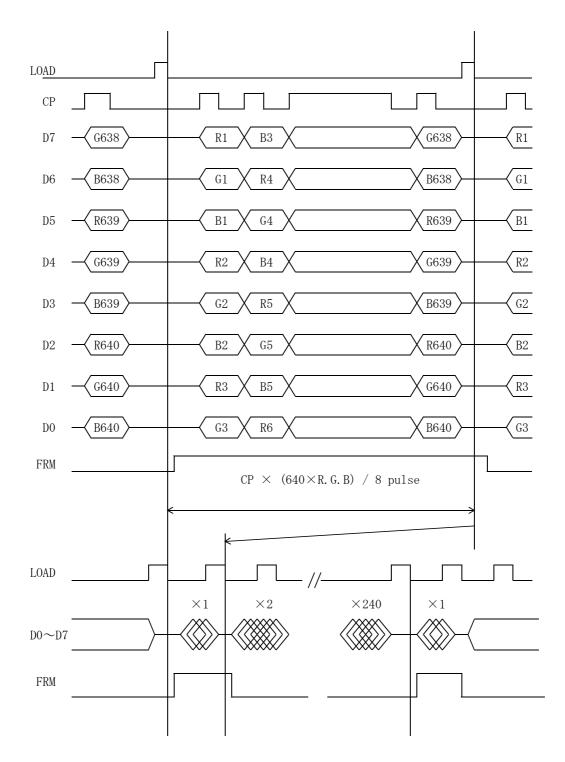
LCD side connector: 08-6210-020-340-800 (ELCO)Recommended matching connector: 0.5mm pitch FFC or FPC

### 8-2. CFL

| PIN No | SYMBOL | DESCRIPTION                 | LEVEL |
|--------|--------|-----------------------------|-------|
| 1      | HV     | Power supply for CFL        | AC    |
| 2      | NC     | _                           | —     |
| 3      | GND    | Ground line (from inverter) | —     |

| LCD side connector             | : | BHR-03VS-1        | (JST) |
|--------------------------------|---|-------------------|-------|
| Recommended matching connector | : | SM02-(8.0)B-BHS-1 | (JST) |

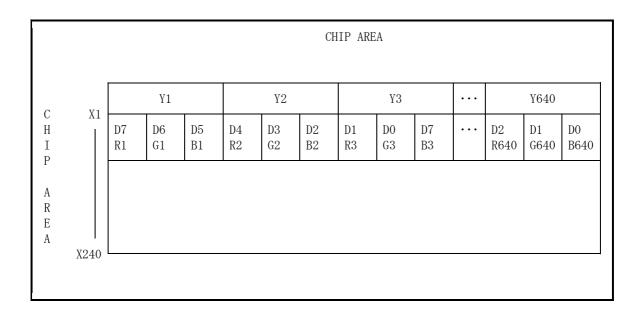
## 9. Interface Timing Chart



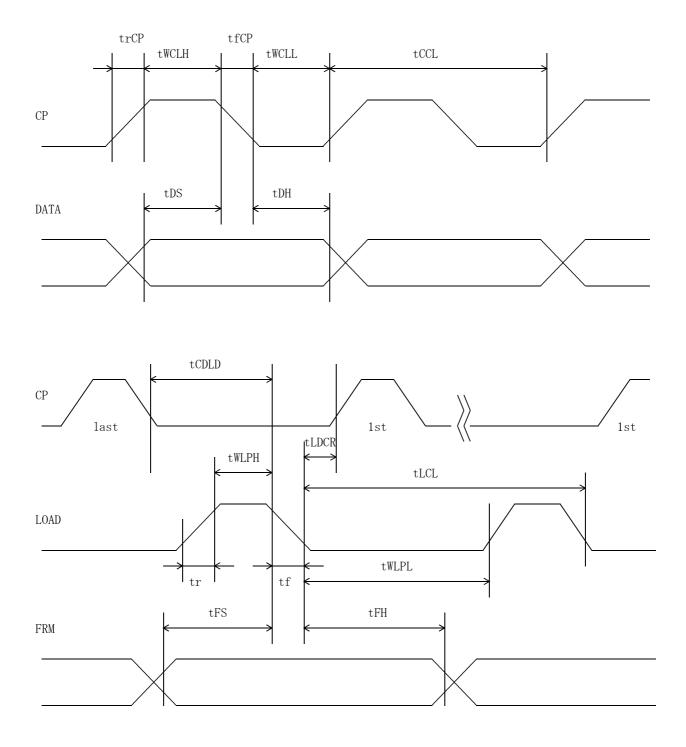
\* The cycle of load signal should be stable and continuously applied without interruption.

\* The above-mentioned timing chart shows a reference to set up a LCD module, not an electrical rating.

## 10. Data and Screen



## 11. Input Timing Characteristics



## 11-1. Switchig characteristics

| ITEM                         | SYMBOL | MIN. | MAX. | UNIT |
|------------------------------|--------|------|------|------|
| CP Cycle *1                  | tCCL   | 170  | —    | ns   |
| CP "H" Pulse Width           | tWCLH  | 80   | —    | ns   |
| CP "L" Pulse Width           | tWCLL  | 80   | —    | ns   |
| CP Rise Up Time              | trCP   | _    | 20   | ns   |
| CP Fall Down Time            | tfCP   | _    | 20   | ns   |
| Data Set Up Time             | tDS    | 60   | _    | ns   |
| Data Hold Time               | tDH    | 60   | —    | ns   |
| LOAD "H" Pulse Width         | tWLPH  | 80   | —    | ns   |
| LOAD "L" Pulse Width         | tWLPL  | 400  | —    | ns   |
| LOAD Cycle *2                | tLCL   | 500  | _    | ns   |
| CP Down→LOAD Down Delay Time | tCDLD  | 100  | —    | ns   |
| LOAD Down→CP Rise Delay Time | tLDCR  | 100  | —    | ns   |
| Input Signal Rise Up Time    | tr     | —    | 20   | ns   |
| Input Signal Fall Down Time  | tf     | -    | 20   | ns   |
| FRM Data Set Up Time         | tFS    | 120  | _    | ns   |
| FRM Data Hold Time           | tFH    | 30   | _    | ns   |

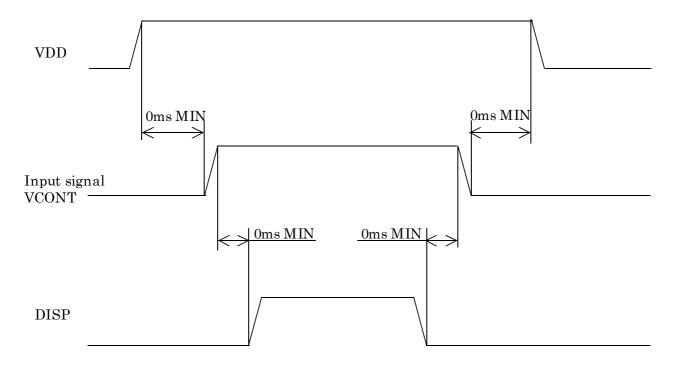
Input Characteristics ; VDD = 3.3V  $\pm$  0.3V, Temp. = 0 ${\sim}50~^\circ\!\mathrm{C}$ 

\*1 CP Cycle is adjust so that FRM signal is 75Hz.

\*2 Load Cycle is const.

#### 12. Supply Voltage Sequence Condition

Always follow the power supply ON / OFF sequence as specified below. Unless you follow the power supply ON / OFF sequences, driving circuit in the LCD may irreparably break and / or DC voltage may be supplied to the LCD. DC voltage induces irreversible electrochemical reations on the screen and reduce LCD life.



- \* Input signal : CP, LOAD, FRM, D0~D7 Each signal (CP,LOAD,FRM) is constant.
- \* The above sequence should be designed as to keep each normal figure on condition that liquid crystal module is loaded on your system.
- \* Control the supply voltage sequence not to float all signal line when the LCD panel is driving.

#### 13. Backlight Characteristics

#### CFL Ratings

Temp. =  $25^{\circ}C$ 

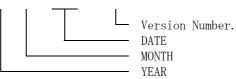
| ITEM                                 | SYMBOL | MIN.       | TYP.       | MAX.       | NOTE  |
|--------------------------------------|--------|------------|------------|------------|-------|
| Starting<br>discharge Voltage        | VS     |            |            | 965 Vrms.  | 0 °C  |
| alscharge voltage *1                 | VS     | _          | _          | 645 Vrms.  | 25 °C |
| Discharging tube current *2,*3       | IL     | 2.0 mArms. | 5.0 mArms. | 6.0 mArms. | _     |
| Discharging tube voltage             | VL     | _          | 395 Vrms.  |            | —     |
| Operating life *4<br>(IL=5.0 mArms.) | Т      | 36,000 h   | 54,000 h   | _          | —     |
| Operating frequency                  | F      | 40 kHz     | _          | 100 kHz    | —     |

- \*1 The Non-load output voltage (VS) of the inverter should be designed to have some margin, because VS may increase due to the leak current which may be caused by wiring of CFL cables. (Reference value: 1255 Vrms MIN.)
- \*2 We recommend that you should set the discharging tube current at lower than typical value so as to prevent the heat accumulation of CFL tube from deteriorating a performance of the LCD.
- \*3 Do not apply more than 6.0mA discharging tube current. Because CFL maybe broken due to over current.
- \*4 When the illuminance or quantity of light has decreased to 50 % of the initial value. Average life time of CFL will be decreased when LCD is operating at lower and higher temperature.
- \* The inverter should meet the eccelentic conditions : sine, symmetric waveform without spike in positive and negative.

#### 14. Lot Number Identification

The lot number shall be indicated on the back of the backlight case of each LCD.

 $\mathrm{K}\,\mathrm{C}\,\mathrm{G}\,\mathrm{0}\,\,\mathrm{6}\,\,\mathrm{2}\,\mathrm{H}\,\mathrm{V}\,\mathrm{1}\,\mathrm{A}\,\mathrm{A}\,\mathrm{-}\,\mathrm{A}\,\mathrm{2}\,\,\mathrm{1}\,\mathrm{-}\quad \Box\quad \Box\,-\,\Box\quad \Box\,-\,\Box$ 



| YEAR  | 2001 | 2002 | 2003 | 2004 | 2005 | 2006 |
|-------|------|------|------|------|------|------|
| CODE  | 1    | 2    | 3    | 4    | 5    | 6    |
|       |      |      |      |      |      |      |
| MONTH | JAN. | FEB. | MAR. | APR. | MAY  | JUN. |
| CODE  | 1    | 2    | 3    | 4    | 5    | 6    |
|       | •    |      |      |      |      |      |
| MONTH | JUL. | AUG. | SEP. | OCT. | NOV. | DEC. |
| CODE  | 7    | 8    | 9    | Х    | Y    | Z    |

#### 1 5. Warranty

15-1. Incoming inspection

Please inspect the LCD within one month after your receipt.

15-2. Production Warranty

Kyocera warrants its LCDs for a period of 12 months after receipt by the purchaser, and within the limits specified. Kyocera shall, by mutual agreement, replace or rework defective LCDs that are shown to be Kyocera's responsibility.

#### 16. Precautions for use

- 16-1. Installation of the LCD
- 1. Please ground the mounting (screw) holes of an LCD module, in order to stabilize brightness and display quality.
- 2. The LCD shall be installed so that there is no pressure on the LSI chips.
- 3. The LCD shall be installed flat, without twisting or bending.
- 4. The display window size should be the same as the effective viewing area.
- 5. In case you use outside frame of effective viewing area as outward appearance of your product, unevenness of its outward appearance is out of guarantee.
- 6. Do not pull the CFL lead wires and do not bend the root of the wires. Housing should be designed to protect CFL lead wires from external stress.
- 16-2. Static Electricity
- 1. Since CMOS ICs are mounted directly onto the LCD glass, protection from static electricity is required. Operation should wear ground straps.

#### 16-3. LCD Operation

- 1. The LCD shall be operated within the limits specified. Operation at values outside of these limits may shorten life, and/or harm display images.
- 2. Vop must be adjusted to optimize viewing angle and contrast.
- 3. Operation of the LCD at temperature below the limit specified may cause image degradation and/or bubbles.

It may also change the characteristics of the liquid crystal.

This phenomenon may not recover. The LCD shall be operated within the temperature limits speci-fied.

#### 16-4. Storage

- 1. The LCD shall be stored within the temperature and humidity limits specified. Store in a dark area, and protected the LCD from direct sunlight or fluorescent light.
- 2. The LCD should be packaged to prevent damage.

16-5. Screen Surface

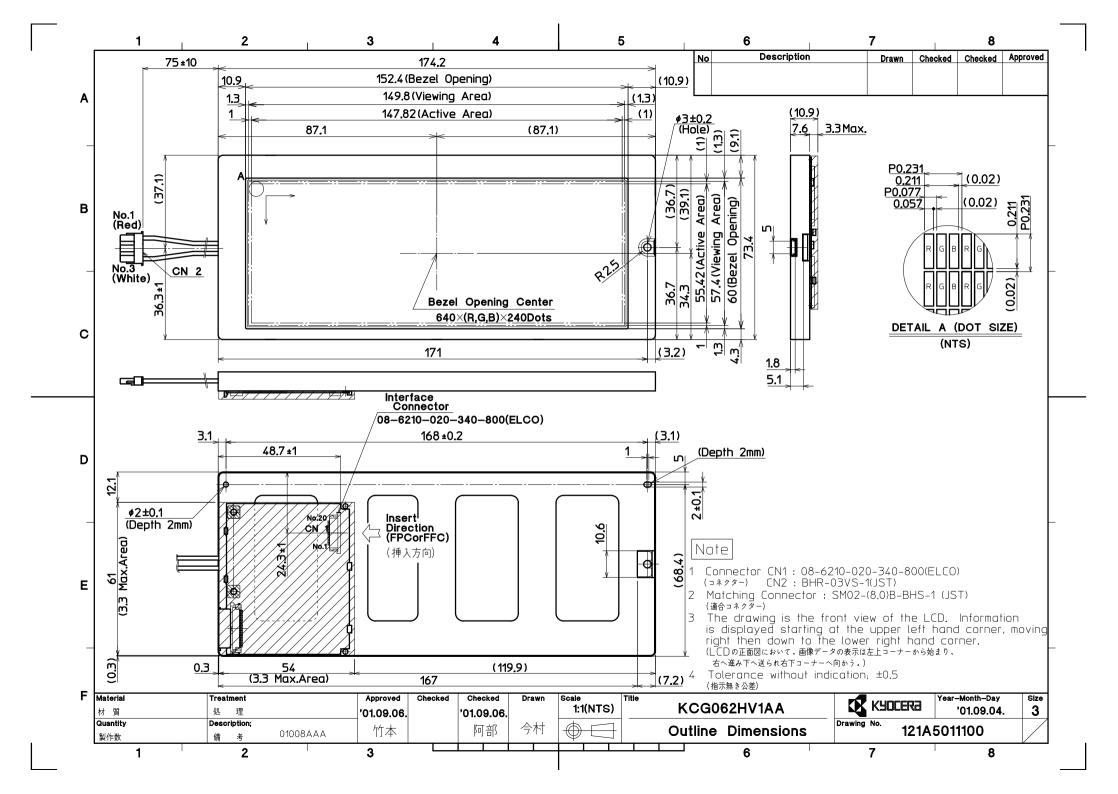
- 1. <u>DO NOT</u> store in a high humidity environment for extended periods. Image degradation, bubbles, and/or peeling off of polarizer may result.
- 2. The front polarizer is easily scratched or damaged. Prevent touching it with any hard material, and from being pushed or rubbed.
- 3. The LCD screen may be cleaned with a soft cloth or cotton pad.
- Methanol, or Isopropyl Alcohol may be used, but insure that all solvent residue is removed. 4. Water may cause damage or discoloration of the polarizer.
- Clean any condensation or moisture from any source immediately. 5. Always keep the LCD free from condensation during testing.
- Condensation may permanently spot or stain the polarizers.

## 17. Reliability Data / Environmental Test

| TEST<br>ITEM                         | TEST<br>CONDITION                     | TEST<br>TIME | RESULT   |
|--------------------------------------|---------------------------------------|--------------|--|
| High Temp.<br>Atmosphere             | 70℃                                   | 240 h        | Display Quality : No defect<br>Display Function : No defect<br>Current Consumption : No defect   |
| Low Temp.<br>Atmosphere              | −20°C                                 | 240 h        | Low Temp. Bubble : None<br>Solid Crystallization of<br>Liquid Crystal : None<br>Display Quality : No defect<br>Display Function : No defect<br>Current Consumption : No defect |
| High Temp.<br>Humidity<br>Atmosphere | 40°C<br>90%RH                         | 240 h        | Display Quality : No defect<br>Display Function : No defect<br>Peel-off of Organic<br>Sealing : None<br>Current Consumption : No defect  |
| Temp. Cycle                          | -20℃ 0.5 h<br>R.T. 0.5 h<br>70℃ 0.5 h | 10 cycles    | Display Quality : No defect<br>Display Function : No defect<br>Peel-off of Organic<br>Sealing : None<br>Bubble on Cell : None  |
| High Temp.<br>Operation              | 50℃<br>Vop                            | 240 h        | Display Quality : No defect<br>Display Function : No defect<br>Current Consumption : No defect   |

\* Each test item uses a test LCD only once. The tested LCD is not used in any other tests.

- $\ast$  The LCD is tested in circumstances in which there is no condensation.
- \* The tested LCD is inspected after 24 hours of storage at room temperature and room humidity after each test is finished.
- \* The reliability test is not an out-going inspection.
- \* The results of the reliability test are for your reference purpose only. The reliability test is conducted only to examine the LCD's capability.



|  |              |               | SPEC.NO.       | TQ3C-8EACO       | -E2CUR05-00 |  |
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|  |              |               | DATE           | December         | 26,2001     |  |
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| KAGOSHIMA HAYATO PLANT<br>LCD DIVISION |              |               |                |                  |             |  |
|  |              |               |                | ·                |             |  |
| Original                               | Designed     | by :Engineeri | ng Dept.       | Confirmed b      | y :QA Dept. |  |
| Issue Data                             | Prepared     | Checked       | Approved       | Checked          | Approved    |  |
| December 26,2001                       | T. Yamaguchi | 4 matsumoto   | H. Chno        | S.Hayoshi        | J. goslita  |  |

Revision Record

| D. I          |      | Design | ed by:  | : Engineering Dept. Confirmed by: QA Dept. |            |          | QA Dept. |
|---------------|------|--------|---------|--|------------|----------|----------|
| Date Prepared |      | red    | Checked | Approved                                   | Checked    | Approved |          |
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# Visuals specification

1)Note

| Item                             | Note  |  |  |  |  |
|----------------------------------|---|--|--|--|--|
| General                          | <ol> <li>When defects specified in this Inspection Standards are<br/>inspected, operating voltage(Vop) shall be set at the<br/>level where optimized contrast is available.<br/>Display quality is applied up to effective viewing area.<br/>(Bi-Level INSPECTION)</li> </ol> |  |  |  |  |
|                                  | applied to any defect w   | ed about the image quality shall be<br>within the effective viewing area<br>cable to outside of the area.        |  |  |  |
|                                  | standard happen, additi   | ch are not specified in this<br>ional standard shall be determined<br>tween customer and Kyocera.                |  |  |  |
|                                  | 4. Inspection conditions  |  |  |  |  |
|                                  | Luminance: 500 Lux minimum .Inspection distance: 300 mm (from the sample)Temperature: $25 \pm 5 \ \mathbb{C}$ Direction: right above  |  |  |  |  |
| Definition of Inspection<br>item | Pinhole, Bright spot<br>Black spot, Scratch<br>Foreign particle   | The color of a small area is<br>different from the remainder.<br>The phenomenon does not change<br>with voltage. |  |  |  |
|                                  | Contrast variation  | The color of a small area is<br>different from the remainder.<br>The phenomenon changes with<br>voltage.         |  |  |  |
|                                  | Polarizer ( Scratch,<br>Bubble, Dent )  | Scratch, Bubble and Dent in the<br>polarizer which can be observed<br>in on / off state.                         |  |  |  |

2)Standard

| Inspection item   | Judgement standard  |                           |              |  |  |
|---|---|---------------------------|--------------|--|--|
| Pinhole, Bright spot<br>Black spot,<br>Foreign particle |   | b ) / 2                   |              |  |  |
|   | Category Size   | (mm)                      | Accentab     | le number                                |  |
|   |   | $d \leq 0.2$              |              | glected                                  |  |
|   |   | d ≦ 0.3                   |              | 5  |  |
|   |   | d ≦ 0.5                   |              | 3  |  |
|   | D 0.5 <   | d                         |              | 0  |  |
| Scratch,Foreign particle                                |   |                           |              |  |  |
|   |   | X                         |              |  |  |
|   | Width (mm)  | Len                       | gth (mm)     | Acceptable No.                           |  |
|   | A $W \leq 0.03$   |                           |              | neglected                                |  |
|   | В   |                           | $L \leq 2.0$ | neglected                                |  |
|   | $\begin{array}{c} C \\ \end{array} 0.03 < W \leq 0.1 \end{array}$ | 2.0 <                     | $L \leq 4.0$ | 3  |  |
|   | D   | 4.0 <                     | < L          | 0  |  |
|   | $E \qquad 0.1 < W$  |                           |              | According tọ<br>Circular                 |  |
| Contrast variation                                      |   | $d \leq 0.5$ $d \leq 0.7$ | neg          | b) / 2<br>le number<br>flected<br>3<br>0 |  |

| Inspection item                        |             | Judgement standard |                |                |  |  |  |
|--|-------------|--------------------|----------------|----------------|--|--|--|
| Polarizer ( Scratch,<br>Bubble, Dent ) | (1) Scratch | L                  | W              |                |  |  |  |
|  | Widt        | h (mm) L           | ength (mm)     | Acceptable No. |  |  |  |
|  | A           | $W \leq 0.1$       |                | neglected      |  |  |  |
|  | B 0.1.5     | < W ≦0.3           | L ≦ 5.0        | neglected      |  |  |  |
|  | C 0.1 <     | 5.0                | < L            | 0              |  |  |  |
|  | D 0.3 <     | < W                | _              | 0              |  |  |  |
|  |             |                    | a<br>d = ( a + | b) / 2         |  |  |  |
|  | Category    | Size (mm)          | Acceptab       | le number      |  |  |  |
|  | А           | $d \leq 0.$        | 2 neg          | lected         |  |  |  |
|  | В           | $0.2 < d \leq 0.$  | 3              | 5              |  |  |  |
|  | С           | $0.3 < d \leq 0.$  | 5              | 3              |  |  |  |
|  |             |                    |                |                |  |  |  |